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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vladimir Berezin et al.

Examiner: Luong Trung Nguyen

Patent No.: 7,372,490

Group Art Unit: 2622

Issue Date: May 13, 2008

Docket No: PBIT-0073.01/US

Title: MULTI JUNCTION APS WITH DUAL SIMULTANEOUS INTEGRATION

Commissioner for Patents  
Office of Patent Publication  
ATTN: Certificate of Correction Branch

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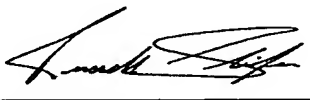
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- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

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Customer Number: 26809

Respectfully submitted,

Date: July 15, 2008

By:   
Russ Slifer  
Reg. No. 39,838

Certificate  
JUL 22 2008  
of Correction

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Christine Hartness

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Name

  
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JUL 22 2008



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**REQUEST FOR CERTIFICATION OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

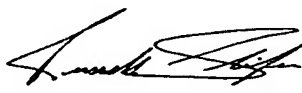
**As the error is that of the Patent Office, it is believed that no fee is due.**

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

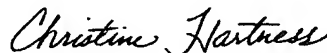
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Christine Hartness



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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,372,490

Page (1) of 1

DATED : May 13, 2008

INVENTOR(S) : Berezin et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, line 11, before ", the" delete "(now allowed)".

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Patent Examination

JUL 22 2008